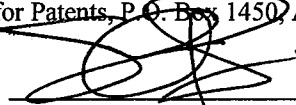


PATENT

I hereby certify that on the date specified below, this correspondence is being filed electronically via EFS-Web addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

October 30, 2008
Date


Sherri A. Phillips

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/822,275

Confirmation No. : 2177

Applicant : Jeffery W. Janzen

Filed : April 8, 2004

Attorney Docket No.: 501286.01 (30262/US)

Art Unit : 2188

Customer No. : 27,076

Examiner : Shawn Eland

Title : SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF
COMPONENTS IN A MULTICHIP MEMORY MODULE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DECLARATION UNDER 37 C.F.R. § 1.131

I, Russell D. Slifer, declare the following:

1. Micron Technology, Inc. is the owner by assignment of the above identified patent application.
2. I am the Chief Patent Counsel at Micron Technology, Inc.
3. Reference Number 2002-1152.00/US (02-1152) is the Micron Technology, Inc. reference number for the above identified application. (See, Exhibit A subject line of letter)
4. Exhibit A shows that a draft of this application, along with an inventor declaration, assignment, and power of attorney documents, was sent by Ed Bulchis, a patent attorney at

Dorsey & Whitney LLP, to Sue Jerome, a patent assistant at Micron Technology, Inc, for her to distribute to the inventor, Jeffrey Janzen, prior to the publication date of the Ryan reference, March 4, 2004. (See, Exhibit A)

5. As shown by Exhibit B, on Saturday February 28, 2004 Jeffrey Janzen sent an email to Dorsey & Whitney paralegal, Denise Sheridan, stating that he had completed his review of the draft application, and that he was ready to sign the declaration and assignment documents. (See, Exhibit B)

6. As shown by Exhibit B, on Monday March 1, 2004, Denise Sheridan sent an email to Jeffrey Janzen stating that she will expect to receive his executed documents from Sue Jerome. (See, Exhibit B)

7. As shown by Exhibit B, on Monday, March 1, 2004, Jeffrey Janzen sent an email to Sue Jerome stating that he had reviewed the application and that he was ready to execute documents for filing. (See, Exhibit B)

8. As shown by Exhibit B, on Wednesday, March 3, 2004, Sue Jerome sent an email to Denise Sheridan requesting copies of the signed documents. (See, Exhibit B)

9. As shown by Exhibit B, on Wednesday, March 3, 2004, Denise Sheridan responded stating that she was under the impression that she should expect the documents from Sue. However, Denise stated that if she received the documents from Jeffrey Janzen, she would forward them to Sue. (See, Exhibit B)

10. As shown by Exhibit B, on Wednesday, March 10, 2004, Sue Jerome emailed Denise Sheridan requesting another set of formal documents. (See, Exhibit B)

11. As shown by Exhibit B, on Wednesday, March 10, 2004, Denise Sheridan resent the formal documents to Sue Jerome via email. (See, Exhibit B)

12. Exhibit C shows that the inventor declaration and assignment documents were signed by Jeffrey Janzen on Friday, March 12, 2004. (See, Exhibit C)

13. Exhibit C shows that on Monday, March 29, 2004 the power of attorney document was signed by Michael Lynch, Chief Patent Counsel at Micron Technology, Inc. at the time this application was filed. (See, Exhibit C)

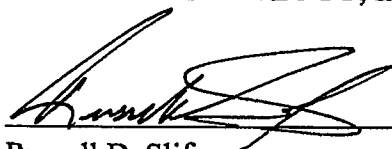
14. Exhibit D shows Sue Jerome sent a letter enclosing the signed assignment, inventor declaration, and power of attorney documents to Dorsey & Whitney LLP on March 30, 2004 by mail. The letter and documents were received at Dorsey & Whitney LLP on Thursday, April 1, 2004. (See, Exhibit D)

15. The application was filed with the USPTO on Thursday, April 8, 2004.

16. The undersigned certifies and declares that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that the making of willfully false statements and the like is punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and may jeopardize the validity of any patent issuing from this patent application

MICRON TECHNOLOGY, INC.

10/30/08
DATE



Russell D. Slifer
Chief Patent Counsel

Enclosures:
Exhibits A – D

EXHIBIT A

DORSEY & WHITNEY LLP

MINNEAPOLIS
NEW YORK
SEATTLE
DENVER
WASHINGTON, D.C.
NORTHERN VIRGINIA
DES MOINES
LONDON
ANCHORAGE
SALT LAKE CITY
BRUSSELS

U.S. BANK CENTRE
1420 FIFTH AVENUE, SUITE 3400
SEATTLE, WASHINGTON 98101
TELEPHONE: (206) 903-8800
FAX: (206) 903-8820
www.dorseylaw.com
EDWARD W. BULCHIS
(206) 903-8785
bulchis.ed@dorsey.com

COSTA MESA
BILLINGS
FARGO
HONG KONG
GREAT FALLS
ROCHESTER
TOKYO
MISSOULA
VANCOUVER
TORONTO
SHANGHAI

VIA FEDERAL EXPRESS

Ms. Sue Jerome
Micron Technology, Inc.
8000 South Federal Way
Boise, Idaho 83716-9632

Re: New Patent Application Entitled
SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF
COMPONENTS IN A MULTICHIP MEMORY MODULE
Your Disclosure No.: 02-1152
Our Reference: 501286.01 (446602-1973)

Dear Sue:

Enclosed are 3 copies of the above-identified patent application, along with a redline version, for review by Mr. Jeffery Janzen to ensure that it is complete and accurate.

If the application requires no revisions, please ask the inventor to sign and date the enclosed Declaration. The Declaration should be signed by Mr. Janzen with his full legal name. The Patent and Trademark Office regulations require that the application be executed in the form in which it is intended to be filed and that the Declaration be attached to and refer to the specification and claims of the application. Please return the signed Declaration and a copy of the application to our office.

DORSEY & WHITNEY LLP

Ms. Sue Jerome
[REDACTED]

Page 2


[REDACTED]
[REDACTED]
[REDACTED]
[REDACTED]

Also enclosed are an original Assignment document which must be executed before a Notary Public and an original Election Under 37 C.F.R. §§ 3.71 and 3.73 and Power of Attorney. Please return the executed Assignment and Election for filing with the Patent Office.

[REDACTED]
[REDACTED]
[REDACTED]
[REDACTED]

Very truly yours,

DORSEY & WHITNEY LLP


Edward W. Bulchis

EWB:dms

Enclosures:

Application (3)
Redline
Declaration
Assignment
Election

[REDACTED]

EXHIBIT B

1 W. 02-1152

Sheridan, Denise

From: Sheridan, Denise
Sent: Wednesday, March 10, 2004 11:20 AM
To: 'sjerome@micron.com'
Subject: RE: 02-1152

Sure. They are attached.
Denise

-----Original Message-----

From: sjerome@micron.com [mailto:sjerome@micron.com]
Sent: Wednesday, March 10, 2004 10:37 AM
To: Sheridan.Denise@dorsey.com
Subject: FW: 02-1152

Could you go ahead and email me a new set of signature papers for this one... I'm thinking since I haven't gotten them back yet, they may very well have been lost somewhere.

Sue

-----Original Message-----

From: jjanzen
Sent: [REDACTED]
To: sjerome
Subject: 02-1152

[REDACTED]

[REDACTED]

Thanks,
Jeff

3/10/2004

Sheridan, Denise

From: Sheridan, Denise
Sent: Wednesday, March 03, 2004 2:09 PM
To: 'sjerome@micron.com'
Subject: RE: Draft Application 02-1152

I assumed that since he is in Boise that he would be sending them to you. If/when I get them from him I will scan and send to you.
Denise

-----Original Message-----

From: sjerome@micron.com [mailto:sjerome@micron.com]
Sent: Wednesday, March 03, 2004 11:51 AM
To: Sheridan.Denise@dorsey.com
Subject: FW: Draft Application 02-1152

Denise..
Could you please email me the signature documents for this one...
Thanks!
Sue

-----Original Message-----

From: jjanzen
Sent: Monday, March 01, 2004 11:27 AM
To: slsummers; sjerome
Subject: FW: Draft Application 02-1152

[REDACTED] my review is complete and it is OK.

Jeff

-----Original Message-----

From: Sheridan, Denise [mailto:Sheridan.Denise@dorsey.com]
Sent: Monday, March 01, 2004 11:22 AM
To: jjanzen
Subject: RE: Draft Application 02-1152

Great. I will be on the look out for the executed documents from Sue. Thanks.

-----Original Message-----

From: jjanzen@micron.com [mailto:jjanzen@micron.com]
Sent: Saturday, February 28, 2004 3:01 PM
To: Sheridan.Denise@dorsey.com
Subject: RE: Draft Application 02-1152

I have completed the review and it is ready for signoff.

Jeff Janzen

-----Original Message-----

From: Sheridan, Denise [mailto:Sheridan.Denise@dorsey.com]
Sent: Friday, February 27, 2004 4:48 PM
To: jjanzen
Subject: Draft Application 02-1152

EXHIBIT C

DECLARATION

As the below-named inventor, I declare that:

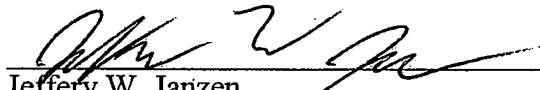
My residence, post office address, and citizenship are as stated below under my name.

I believe I am the original, first, and sole inventor of the invention entitled "SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF COMPONENTS IN A MULTICHIP MEMORY MODULE," which is described and claimed in the foregoing application and for which a patent is sought.

I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to herein (if any).

I acknowledge my duty to disclose information of which I am aware which is material to patentability and examination of this application in accordance with 37 C.F.R. § 1.56(a).

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that the making of willfully false statements and the like is punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and may jeopardize the validity of any patent issuing from this patent application.


Jeffery W. Janzen

Date 3-12-04

Residence : City of Meridian, County of Ada
State of Idaho
Citizenship : United States of America
P.O. Address : 2727 North Silverleaf Way
Meridian, Idaho 83642

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jeffery W. Janzen

Filed : Concurrently herewith

For : SYSTEM AND METHOD FOR
OPTIMIZING INTERCONNECTIONS
OF COMPONENTS IN A MULTICHIP
MEMORY MODULE

Docket No. 501286.01

Disclosure No. 02-1152

A S S I G N M E N T :

☒ Enclosed for recording☐ Previously recorded

Date: _____

Reel: _____

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned does hereby:

SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled **"SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF COMPONENTS IN A MULTICHIP MEMORY MODULE"**; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;


Jeffery W. Janzen

STATE OF IDAHO)
) ss.
County of *Ada*)

Kathleen Louise Webster
Notary or Consular Officer
My Commission Expires 9-6-06

Page 2 of 2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jeffery W. Janzen Attorney Docket No.: 501286.01
Filed : Concurrently herewith
Title : SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF COMPONENTS
IN A MULTICHIP MEMORY MODULE

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ELECTION UNDER 37 C.F.R. §§ 3.71 AND 3.73(b) AND POWER OF ATTORNEY

Sir:

The undersigned, being Assignee of the entire interest in the above-identified application by virtue of an Assignment filed concurrently herewith, hereby elects, under 37 C.F.R. § 3.71, to prosecute the application to the exclusion of the inventor.

Assignee hereby appoints EDWARD W. BULCHIS, Reg. No. 26,847; PAUL T. MEIKLEJOHN, Reg. No. 26,569; KIMTON N. ENG, Reg. No. 43,605; BRIAN C. PARK, Reg. No. 45,519; MARK W. ROBERTS, Reg. No. 46,160; STEVEN H. ARTERBERRY, Reg. No. 46,314; PAUL F. RUSYN, Reg. No. 42,118; JAMES ACKLEY, Reg. No. 45,695; RONALD BROWN, Reg. No. 29,016; DAVID BRUHN, Reg. No. 36,762; KEVIN CHAPPLE, Reg. No. 44,072; W. ROBINSON CLARK, Reg. No. 41,530; SHANE COLEMAN, Reg. No. 44,623; GREGORY DURBIN, Reg. No. 42,503; DANIEL FISHER, Reg. No. 34,162; DAVID FRONEK, Reg. No. 25,678; ANTHONY GANGEMI, Reg. No. 42,565; MICHAEL GILCHRIST, Reg. No. 40,619; THERESA HANKES, Reg. No. 45,501; JOHN HARROP, Reg. No. 41,817;

BRAD HATTENBACH, Reg. No. 42,642; REED HEIMBECHER, Reg. No. 36,353; CRAIG HEMENWAY, Reg. No. 44,759; STUART HEMPHILL, Reg. No. 28,084; MARK HOGGE, Reg. No. 31,622; JOHN KENNEDY, Reg. No. 42,717; JASON KRAUS, Reg. No. 42,765; BRIAN LAURENZO, Reg. No. 34,207; KENNETH LEVITT, Reg. No. 39,747; KURT LEYENDECKER, Reg. No. 42,799; SCOTT MARKS, Reg. No. 44,902; KEITH MONTGOMERY, Reg. No. 45,245; ALDO NOTO, Reg. No. 35,628; LEE OSMAN, Reg. No. 38,260; DEVAN PADMANABHAN, Reg. No. 38,262; JAMES PINTO, Reg. No. 40,774; GARY POLUMBUS, Reg. No. 25,364; SCOTT ROTHENBERGER, Reg. No. 41,277; LEONARD SANTISI, Reg. No. 24,135; ROBERT SCHEFFEL, Reg. No. 43,090; AMI SHAH, Reg. No. 41,143; MIRIAM SOHN, Reg. No. 35,368; HERMES SOYEZ, Reg. No. 45,852; GERALD SULLIVAN, Reg. No. 37,243; MARK THOMAS, Reg. No. 37,953; JON TUTTLE, Reg. No. 25,713; MEGAN VALENTINE, Reg. No. 47,149; LANCE VIETZKE, Reg. No. 36,708; SEAN WOODEN, Reg. No. 43,997; and the attorneys and agents associated with the firm of **DORSEY & WHITNEY LLP**, **Customer Number 27,076**, along with MICHAEL L. LYNCH, Reg. No. 30,871; CHARLES B. BRANTLEY, II, Reg. No. 38,086; KEVIN D. MARTIN, Reg. No. 37,882; and DAVID J. PAUL, Reg. No. 34,692, of MICRON TECHNOLOGY, INC., 8000 South Federal Way, Boise, Idaho 83716-9632, as its attorneys to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. Please direct all telephone calls to **Edward W. Bulchis** at (206) 903-8800 and telecopies to (206) 903-8820.

Please direct all communications to:

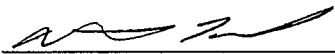
Edward W. Bulchis, Esq.
Customer No. 27,076
 Dorsey & Whitney LLP
 1420 Fifth Avenue, Suite 3400
 Seattle, Washington 98101
 (206) 903-8800

Pursuant to 37 C.F.R. § 3.73(b), the undersigned duly authorized designee of Assignee certifies that the evidentiary documents have been reviewed, specifically the Assignment to MICRON TECHNOLOGY, INC., filed concurrently herewith for recording, a copy of which is

attached hereto, and certifies that to the best of my knowledge and belief, title remains in the name of the Assignee.

MICRON TECHNOLOGY, INC.
ASSIGNEE

March 29, 2008
DATE


Michael L. Lynch
Chief Patent Counsel

Enclosure:

Copy of Assignment

c:\ndeloc\temp.pogson.phoebe\501286.01 election & poa.doc

EXHIBIT D



Micron Technology, Inc.
8000 S. Federal Way
P.O. Box 6
Boise, ID 83707-0006
208.368.4000

March 30, 2004

RECEIVED

APR 01 2004

DORSEY & WHITNEY LLP

Denise Sheridan
Dorsey & Whitney
US Bank Center
1420 5th Avenue, Suite 3400
Seattle, WA 98101

RE: Micron Reference No.: 02-1152
Your Reference No.: 501286.01
SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF
COMPONENTS IN A MULTICHIP MEMORY MODULE

Dear Denise:

Enclosed please find the Assignment, Declaration and Power of Attorney, for the above case, which have been executed by the appropriate parties.

I believe this case should now be ready for filing with the Patent Office.

If you have any questions, please do not hesitate to call.

Very truly yours,

A handwritten signature in cursive script, appearing to read "Susan Jerome".

Susan Jerome
Patent Assistant